



# AF 2800

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2815  
Serial No.: 09/736,724 Examiner : Paul E. Brock  
Filed : December 13, 2000  
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

# 8/Ext 2  
10/10/02  
✓**BOX AF**

Commissioner for Patents  
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.  
The required fees are computed below. Please apply any charges  
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	73	-	73	=	0	\$0
Independent	16	-	16	=	0	\$0

Applicant hereby petitions under 37 C.F.R.  
\$1.136 for a 2 month extension of time.

\$400

TOTAL FEE DUE

\$400

A check for \$400 is attached.

Respectfully submitted,

Date: 9/26/02

Linda G. Gunderson  
Reg. No. 46,341

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## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

10/04/2002 AWONDAF1 00000068 09736724

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400.00 OP

I hereby certify under 37 CFR §1.8(a) that this correspondence is being  
deposited with the United States Postal Service as first class mail with  
sufficient postage on the date indicated below and is addressed to the  
Commissioner for Patents, Washington, D.C. 20231.

September 26, 2002

Date of Deposit

Signature

  
Norman Green

Typed or Printed Name of Person Signing Certificate